

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No	
Filing Date	August 31, 2000
Inventor	Garo J. Derderian, et al.
Assignee	Micron Technology, Inc.
Group Art Unit	2818
Examiner	T. Lee
Attorney's Docket No	MI22-1330
Title: Capacitor Fabrication Methods Including Forming a	a Conductive Layer

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

To:

Assistant Commissioner for Patents

Washington, D.C. 20231

From:

James E. Lake (Tel. 509-624-4276; Fax 509-838-3424)

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Spokane, WA 99201-3828

References - See attached Form PTO-1449

In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, your attention is directed to the United States patents and other references listed on the attached Form PTO-1449. No admission is made regarding whether all the submitted references are prior art.

Citation of these references is respectfully requested.

Respectfully submitted,

Dated:

15 Aug 2003

Bv

James E. Lake

Form PTO-P	49/1		ATTY, DOCKET MI22-1330		SERIAL NO. 09 653,149					
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*Examiner Initial		Document Number	Date	Name		Class		Filing Date If Appropriate		
	.1.1	6,222,722	04/99	Fukuzumi et al						
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considered. Include copy of this form with next communication to applicant.

U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE

ATTY. DOCKET NO. MI22-1330 SERIAL NO. 09/653,149

LIST OF ART CITED BY APPLICANT

(Use several sheets if necessary)

FILING DATE August 31, 2000

APPLICANT Garo J. Derderian

GROUP 2818

U.S. PATENT DOCUMENTS

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*Examiner Initial		Document Number			Name		Subclass	Filing Date If Appropriate	
	AA	09/590,795		Agarwal et 1/6/03)	al (as filed and amended 10/29/01 &			06:00	
	AB	09:653,156		Agarwal (a	s amneded 2'18'2003)				
	AC								
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